Modeling Electrical Systems

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Agenda

- Basic Electrical Elements
- Passive Circuit Analysis
- Active Circuit Analysis
- Case Study: A Speaker Model

 Active Learning: Pair-share Exercises, Case Study

Basis Electrical Elements

Electrical System

- Composed of resistors, capacitors, inductors, transistors, amplifiers, power supplies
 - Passive circuits: respond to applied voltage or current and do not have any amplifiers
 - Active circuits: made of transistors and/or amplifiers, require active power source to work
- Basic quantities
 - Charge q [coulomb] = 6.24×10^{18} electrons
 - Current i [ampere] = dq/dt
 - Voltage e [Volt] = dw/dq
 - Energy or Work w [joule]
 - Power p [watt] = e x i = dw/dt

Qunatity and Symbol	Units	Representation
Voltage v	volt (V)	Voltage $v \xrightarrow{+} - $
Charge Q	coulomb = newton-meter/volt	
Current i	ampere $(A) = coulomb/second$	Current $i \oint$
Flux ϕ	volt-second	Ū
Resistance <i>R</i> Capacitance <i>C</i> Inductance <i>L</i>	ohm (Ω) = volt/ampere farad (F) = coulomb/volt henry (H) = volt-second/ampere	
Battery (voltage source plus internal resistance)	_	+
Ground (zero voltage		Î

Units and Representations for Common Electrical Quantities

reference)

General Model Structure for Mechanical System





Equivalent Circuit for Spring-Mass-Dashpot Systems



Connection rule for the equivalent circuit for $e \rightarrow V$ convention:

Resistance

• Resistance behavior is between insulator and conductors, allowing a predictable restriction of electron flow $e_1 = \frac{R}{e_1} = \frac{e_2}{e_2}$

$$\delta e = Ri$$
 where $\delta e = e_1 - e_2$

$$\delta e = e_1 - e_2$$

- Power dissipated = $\delta e i$
- Resistance $R = \frac{\rho \ell}{A}$

A: cross section are of wire

- I: length of wire
- $-\rho$: resistivity of material

Capacitance

 Capacitor stores electrons on 2 parallel plates separated by an insulating dielectric material in an electric field

$$\delta e = \frac{1}{C} \int_{-\infty}^{t} i \, dt$$
 or $i = C \frac{d \, \delta e}{dt}$

$$w = \int_{-\infty}^{t} \delta e \ i \ dt = \int_{-\infty}^{t} \delta e \left(C \frac{d \ \delta e}{dt} \right) dt = \int_{0}^{\delta e} C \ \delta e \ d \ \delta e = \frac{C \ \delta e^{2}}{2}$$

- Capacitance $C = \frac{\epsilon A}{d}$
 - A: area of plates
 - D: spacing between plates
 - ϵ :permittivity of the dielectric

Inductance

- Inductance relates voltage induced to time rate of change of magnetic field
- Faraday's law:

 $e_i = \frac{d\phi}{dt}$ where ϕ is the magnetic flux, ϕ = Li

$$\delta e = L \frac{di}{dt}$$
 where $\delta e = e_1 - e_2$ $\stackrel{e_1 \quad L \quad e_2}{\longrightarrow i}$

- Energy stored $w = \int_{-\infty}^{t} \delta e \, i \, dt = \int_{-\infty}^{t} \left(L \frac{di}{dt} \right) i \, dt = \int_{0}^{t} Li \, di = \frac{Li^2}{2}$
- Inductance:

 $L = \frac{\mu_m n^2}{\ell} A$ where A= wire cross section area, I = wire length, n = number of turns, μ_m =permeability of magnetic circuit

Impedance

• Impedance Z: instantaneous ratio of voltage difference to current

$$Z = \frac{\delta e}{i}$$

- Impedance of common circuit elements
 - Resistive: $Z_r = R$ (not dynamic)
 - Capacitive: $Z_c = 1/CD$
 - Inductive: Z_L =LD

Where D= differential operator d/dt,

1/D = integrator operator

Ideal and Non-Ideal Sources



Open and Short Circuits

• An open circuit is any element through which current cannot flow



 A short circuit is any element across which there is no voltage



Series and Parallel Impedance Combinations



Laws for Passive Circuit Analysis

- Kirchhoff's Current Law (KCL)
 - The sum of all currents (flow) entering a node is zero
- Kirchhoff's Voltage Law (KVL)
 - The oriented sum of all voltages (efforts) around any closed loop is zero





Techniques for Passive Circuit Analysis for Classical Deriving Differential Equations

- 1. Draw circuit schematic and label components (e.g., R_1 , R_2 , C_1 , L_1 ...)
- 2. Assign voltage at each node (e.g., e_1 , e_2)
- 3. Assign current in each component (e.g., i_1 , i_2 , ..) and show positive current direction with arrows
- 4. Write equation for current for each component (e.g., $i_{R1} = (e_1 e_2)/R_1$ or $i_{C1} = CDe_1$)
- 5. Write node equations for each significant node (not connected to voltage or current source)
- Substitute component equations into node equations and reduce results to a single differential equation with output and input variables

Example 1: Voltage Divider

Evaluate e₁

$$i_1 = \frac{e_0 - e_1}{R_1}$$
$$i_2 = \frac{e_1 - 0}{R_2}$$
$$i_1 - i_2 = 0$$

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$$\frac{e_0 - e_1}{R_1} - \frac{e_1}{R_2} = 0$$
$$e_1 = \frac{R_2}{R_1 + R_2} e_0$$

$$e_1 = \frac{1}{1 + \frac{R_1}{R_2}} e_0$$



Example 2: Resistor Circuit

• Calculate the amount of power dissipated in resistor R_3 in the circuit shown below R_1



• Solution:

resistors in parallel
$$R_{eq} = \frac{R_2 R_3}{R_2 + R_3} = \frac{1000 \times 220}{1000 + 220} = 180.3 \,\Omega$$

voltage divider equation

$$e_1 = \frac{R_{eq}}{R_1 + R_{eq}} e_0 = \frac{180.3}{100 + 180.3} 10 \text{ volts} = 6.43 \text{ volts}$$

power equation

$$P = \frac{e_1^2}{R_3} = \frac{6.43^2 \text{ volt}^2}{220\left(\frac{\text{volt}}{\text{amp}}\right)} = 0.188 \text{ watt}$$

Example 3: Pair-Share Exercise Seven-Resistor Circuit

 The resistive circuit shown consists of a voltage source connected to a combination of seven resistors. The output is voltage e₀. Find the equivalent resistance R_{eq} of the sevenresistor combination and evaluate e₀.



Example 3: Pair-Share Exercise Seven-Resistor Circuit



$$R_{\rm eq} = \frac{1}{2} + \frac{5}{2} = 3\,\Omega$$

Apply voltage divider equation repeatedly

$$e_A = \left(\frac{\frac{5}{2}}{\frac{1}{2} + \frac{5}{2}}\right)e_i(t) = \frac{5}{6}e_i(t)$$





- Write the modeling equations for circuit (a)
- Derive the differential equation in the form $[\tau D+1]e_1 = Ge_0$
- What are the mathematical expressions for time constant $\boldsymbol{\tau}$ and gain G?
- For circuit (b), is the differential equation for this circuit a product of two RC's circuit, that is, [τ₁D+1][τ₂D+1]e₂=e₀ ?

For the circuit of Figure P4.12a, (a) nponent equations are:

$$i_{R_1} = \frac{e_0 - e_1}{R_1}, \quad i_{C_1} = C_1 D e_1 \quad \text{with } e_1(0)$$

The node equation: $i_{R_1} = i_{C_1}$

Substituting component into node equation:

$$(R_1 C_1 D + 1) e_1 = e_0$$

The time constant is $\tau = R_1 C_1$

The static gain is G = 1



(a)



For the circuit of Figure (b) , the component equations are:

$$i_{R1} = \frac{e_0 - e_1}{R_1}$$
 $i_{C_1} = C_1 D e_1$ with $e_1(0)$

$$i_{R_2} = \frac{e_1 - e_2}{R_2}$$
 $i_{C_2} = C_2 D e_2$ with $e_2(0)$

The node equations: $i_{R_1} = i_{C_1} + i_{R_2}$ $i_{R_2} = i_{C_2}$

Substituting component into node equations:

$$\frac{e_0 - e_1}{R_1} = C_1 D e_1 + \frac{e_1 - e_2}{R_2} \quad \text{or} \quad \left(R_1 C_1 D + 1 + \frac{R_1}{R_2}\right) e_1 = e_0 + e_2$$
$$\frac{e_1 - e_2}{R_2} = C_2 D e_2 \quad \text{or} \quad (R_2 C_2 D + 1) e_2 = e_1$$

Combining and using $\tau_1 = R_1 C_1$ and $\tau_2 = R_2 C_2$:

$$\left(\tau_{1} D + 1 + \frac{R_{1}}{R_{2}}\right)(\tau_{2} D + 1)e_{2} - e_{2} = e_{0}$$

$$\left[\tau_{1}\tau_{2}D^{2} + \left(\tau_{1} + \tau_{2} + \frac{R_{1}}{R_{2}}\right)D + \frac{R_{1}}{R_{2}}\right]e_{2} = e_{0}$$

From the above, we can see that the transfer function is not the product of the two individual transfer functions. This is due to the current being drawn from the first RC to drive the second.