Pierce College

CSIT 546

Take Home
Midterm Examination
31 March 2015

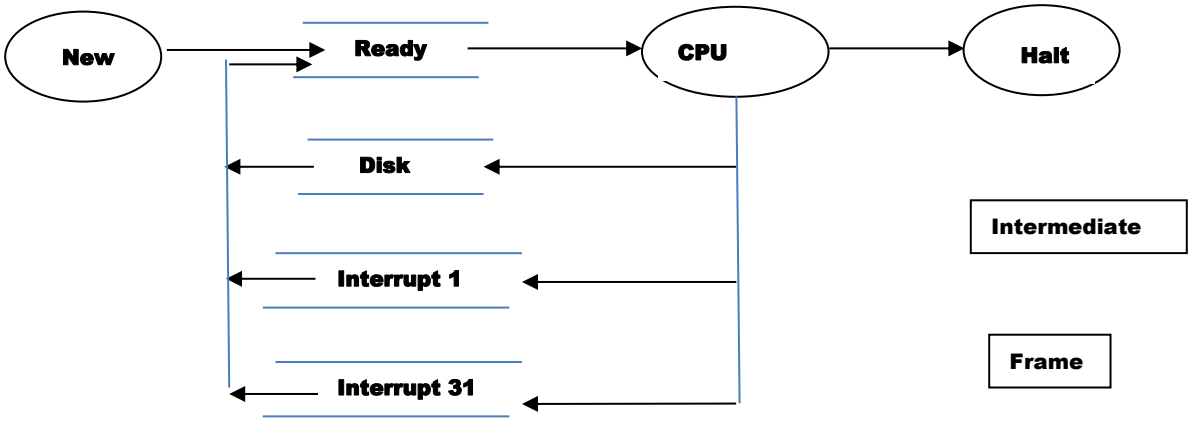
Name:_____

1. **Current Technology Systems**

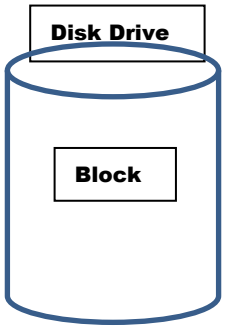
   a. **What is the operational difference between a 4-Core chip using Point to Point Interconnect with PCI Express and a System that uses four Processors with a High-Speed Asynchronous Bus?**

   b. **How does an ARM chip and an Intel chip differ from one another?**

2. **Given the processing cycle below, indicate where and how the following components are integrated into the system. Describe this concept in detail.**



   a. **Long Term Scheduling**
   b. **Medium Term Scheduling**
   c. **Short Term Scheduling**
   d. **I/O Scheduling**
   e. **Swapping**
   f. **Paging**



Stallings

3. **page 62 #2.9**
4. **page 109 #3.3**
5. **page 219 problem 6.5**
6. **page 219 problem 6.10**
7. **page 261 problem 7.6**
8. **page 262 problem 7.7**
9. **page 318 problem 9.8**
10. **page 318 problem 9.15**